

JUL-1449 REPRODUCED

ATTORNEY DOCKET NO.  
2789.2004-001APPLICATION NO.  
09/557,164INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION

(07/05/00)

(Use several sheets if necessary)

APPLICANT

William J. Dally et al.

FILING DATE  
04/25/00

GROUP

2838

2631

## PATENT DOCUMENTS

EXAM- INER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
AA						

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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AS	Garcia, F. et al., "Design of a Slew Rate Controlled Output Buffer," IEEE 0-7803-4980, 1998, pp. 147-150.
AT	Raver, Norman, "Open-Loop Gain Limitations for Push-Pull Off-chip Drivers," IEEE Journal of Solid-State Circuits, Vol. SC-22, no. 2, April 1987, pp. 145-150.
AU	Hashimoto, Masashi et al., "Low dI/dt Noise and Reflection Free CMOS Signal Driver," IEEE 1989 Custom Integrated Circuits Conference, 1989, pp. 14.1.1-14.4.4.
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AY	Senthinathan, R., et al., "Application Specific CMOS Output Driver circuit Design Techniques to Reduce Simultaneous Switching Noise," IEEE Journal of Solid-State Circuits, Vol. 28, No. 12, 1993, pp.1383-1388.
AZ	Furutani, K., et al., "An Adjustable Output Driver with a Self-Recovering Vpp Generator for a 4M x 16 DRAM," IEEE Journal of Solid-State circuits, Vol. 29, No. 3, 1994, pp. 308-310.
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EXAMINER

DATE CONSIDERED

5/30/03